

# OCP-IP

## DATE Pavilion Schedule

Open Sessions

|               | Tuesday, March 8  | Wednesday, March 9  | Thursday, March 10 |
|---------------|---|---|--------------------|
| 11:30 – 12:00 | <b>Trace Instrumentation and Architectures for OCP Buses</b><br>First Silicon Solutions/MIPS  | <b>A New SCV Compliant Transaction Recording Monitor for the SystemC OCP Channel</b><br>CoWare  |                    |
| 12:00 – 12:30 | <b>Interface Verification and Compliance</b><br>Texas Instruments   | <b>System-Level Design with OCP</b><br>Nokia  |                    |
| 12:40 – 13:50 | <b>Sonics Panel: Network on Chip: Great Idea, but Where are the Implementations?</b><br>Panelists:<br>• CoWare, Heinz Holzapfel<br>• Philips, Dr. Kees Goossens<br>• Sonics, Drew Wingard<br>• Tampere University of Technology, Jari Nurmi | <b>Panel: New Paradigm for Design Excellence: What's on the Horizon?</b><br>Panelists:<br>• Cadence, Ted Vucurevich<br>• Sonics, Grant Pierce<br>And other participants<br>• Moderator: Gary Smith, Gartner         |                    |
| 13:50 – 14:30 | <b>A Modest Approach to the IP Business - That's Succeeded for Over Ten Years</b><br>CAST   | <b>Leveraging OCP to Create a Cohesive IP Infrastructure for Rapid and Successful SoC Design</b><br>Sonics  |                    |
| 14:30 – 15:10 | <b>New Methodologies for IP and SoC Validation</b><br>ProDesign   | <b>High-performance OCP Multi-channel SDRAM Memory Controller Modeling at RTL and Transactional Levels</b><br>Prosilog  |                    |
| 15:10 – 16:00 | <b>A Key Focus of Embedded Design is the Provision of New, High Quality, High Performance Services to Mobile Users</b><br>Accent  | <b>Integrating Analog/Mixed-Signal IP Blocks in SoCs</b><br>LTRIM   |                    |
| 16:10 – 17:20 | <b>CAST Panel: IP From Silicon Vendors - Good or Bad?</b><br>Panelists:<br>• CAST, Bill Finch<br>• Altera Europe, Pat Mead<br>And other participants<br>• Moderator: Andy Travers, VCX Software   | <b>Panel: What is EDA Missing?</b><br>Panelists:<br>• Celoxica, Jeff Jussel<br>• Nascentric, Vess Johnson<br>• SEMI, Tom Grebinski<br>And other participants<br>• Moderator: Ulf Schlichtmann, University of Munich |                    |
| 17:20 – 17:50 | <b>A Comprehensive Approach for Verification of OCP-based SoC</b><br>YogiTech   | <b>Automatic Compliance Checking, Testbench Enhancement and Specification Coverage for OCP-based Designs</b><br>TransEDA  |                    |

